



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

AKIYOSHI

Art Unit: 2816

Application No.: 09/769,534

Examiner: ENGLUND, T.

Filed: January 26, 2001

Attorney Dkt. No.: 108397-00025

For: POWER-ON RESET CIRCUIT/METHOD FOR INITIALIZING
AN INTEGRATED CIRCUIT (As Amended)

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PRELIMINARY AMENDMENT WITH FILING OF AN RCE

Commissioner for Patents
Washington, D.C. 20231

Date: August 22, 2002

Sir:

In response to the Office Action dated April 22, 2002, having been duly extended from July 22, 2002 until August 22, 2002, with the filing of the attached Petition for Extension of Time, please amend the above-identified application as set forth below.

IN THE CLAIMS:

Please amend claims 2, 4 and 6 as follows:

2. (Twice Amended) A semiconductor integrated circuit according to claim 1, wherein said main reset signal generator comprises:

a plurality of pulse generators for respectively generating pulses on the basis of a transition edge for a corresponding one of said sub power-on reset signals; and

a composite circuit for synthesizing the pulses to generate said main power-on reset signal.

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